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CONTROL METHOD AND ELECTRONIC DEVICE

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] The present invention claims priority from Japanese Application Nos. P2000-111030 filed April 12, 2000 and P2001-058598 filed March 2, 2001, the disclosures of which are hereby incorporated by reference herein.

BACKGROUND OF THE INVENTION

[0002] The present invention relates to control methods and electronic devices suitable for being used as, for example, devices which are removably provided with respect to a main unit in order to perform arbitrary functions. More particularly, the present invention relates to a control method and to an electronic device which has an equivalent shape to a memory card or a similar semiconductor memory and which is connected to the memory connecting section of the main unit in order to perform arbitrary functions.

[0003] For example, an electronic device may have an equivalent shape to a memory card which is removable from a main unit or to a similar semiconductor memory. The electronic device is connected to the memory connecting section of the main unit in order to perform arbitrary functions. Specifically, such an electronic device adds on a connecting function for a computer network or a communication function. In this way, the electronic device easily enhances the operation and application of the main unit.

[0004] If the main unit does not recognize a function to be performed by the electronic device, inappropriate data may be transmitted and, hence, erroneous processing may be performed. At the main unit side, a malfunction may result. When the main unit requests a function not installed in the electronic device, that function cannot be executed and, as a result, the main unit may go down. Also, it may require time and a complicated procedure to restore the main unit to its original state.

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[0005] The problems with the conventional electronic device include erroneous processing and main unit malfunctions caused by the inability of the main unit to recognize a function to be executed by the electronic device. When a function is not executed, the main unit may go down, and it may require time and a complicated procedure to restore the main unit to its original state.

SUMMARY OF THE INVENTION

[0006] Accordingly, it is an object of the present invention to provide, in a register for performing setting, a write area in which the code of a function requested by a main unit is written, and a read area in which the code of a function selected in an electronic device and the code associated therewith are read. Accordingly, it is possible to enable a main unit to recognize a function executed by the electronic device by using a simple method, and hence a desired function can be reliably executed. Thus, the main unit can reliably enable a desired function.

[0007] According to an aspect of the present invention, a control method for a main unit and an electronic device removably connected thereto is provided. The method includes providing a register in the electronic device, the register having a write area and a read area and performing setting when a function is executed. The code of a function requested by the main unit is written in the write area of the register. The code of a function to be executed and the code associated therewith are read in the read area of the register.

[0008] According to another aspect of the present invention, an electronic device is provided which is removably connected to a main unit for exchanging arbitrary data with the main unit and for executing a plurality of functions. The electronic device includes a register for performing setting when ones of the plurality of functions are executed. The register includes a write area in which the code of a function

requested by the main unit is written, and a read area in which the code of a function selected in the electronic device and the code associated therewith are read.

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~~[0009] According to the present invention, it is possible to enable a main unit to recognize a function to be executed by an electronic device, and hence a desired function is reliably executed by using a simple method. The main unit can reliably enable a desired function. Also, the main unit can easily recognize a function installed in the electronic device. It is therefore possible to prevent unnecessary operations and malfunctions such as the activation of an unexecutable function. Even when a list of functions is not provided, it is still possible to prevent unnecessary operations and malfunctions such as the activation of an unexecutable function. As a result, the present invention can solve problems encountered with a conventional electronic device. Such problems include erroneous processing and main unit malfunctions due to the main unit's inability to recognize a function to be executed by the electronic device, which may require time and a complicated procedure to restore the main unit to its original state.~~

BRIEF DESCRIPTION OF THE DRAWINGS

[0010] Fig. 1 is a block diagram of an electronic device and a main unit according to an embodiment of the present invention;

[0011] Fig. 2 is an external view of an electronic device according to an embodiment of the present invention;

[0012] Fig. 3 is a diagram illustrating an example of the memory register of the electronic device;

[0013] Fig. 4 is a diagram illustrating an example of the category and class addresses of the electronic device;

[0014] Fig. 5 is a diagram illustrating an example of the boot block format of the memory of the electronic device;

[0015] Fig. 6 is a diagram illustrating an example of the system entry area of the electronic device; and

[0016] Fig. 7 is a diagram illustrating the function list data of the electronic device.

DETAILED DESCRIPTION

[0017] The present invention will become apparent from the following description of the preferred embodiment with reference to the accompanying drawings. Fig. 1 is a block diagram of an electronic device and a main unit according to an embodiment of the present invention.

[0018] Referring to Fig. 1, a main unit 100 is illustrated at the left of the block diagram. A data file stored in a memory device (not shown) or the like provided in the main unit 100 is exchanged with a memory interface (I/F) 11 through a file manager 10. An add-on driver 12 is provided for performing an arbitrary extended function. Data in the add-on driver 12 is exchanged with an add-on interface 13. Data in the memory interface 11 and the add-on interface 13 is exchanged with a first serial interface 14.

[0019] At the right of the block diagram, an electronic device 200 is shown. Referring to Fig. 2, for example, the electronic device 200 has an equivalent shape to a so-called memory card or a semiconductor memory similar to the memory card. The electronic device 200 is removably provided with respect to the main unit 100. The electronic device 200 is electrically connected to the main unit 100 through a contact group 201 provided at the end of the electronic device 200. Thus, data can be exchanged between the main unit 100 and the electronic device 200.

[0020] Specifically, referring back to Fig. 1, data is exchanged between a second serial interface 20 provided in the electronic device 200 and the first serial interface 14 provided in the main unit 100. In a conventional memory, the second serial interface 20 exchanges data with a memory

register 21 and a first data buffer 22. In accordance with a setting which is written in the memory register 21, data in the first data buffer 22 is exchanged with a memory 24 through a memory controller 23.

[0021] When performing an extended function, the second serial interface 20 exchanges data with an add-on register 25 and a second data buffer 26. In accordance with a setting which is written in the add-on register 25, data in the second data buffer 26 is exchanged with an add-on unit 28 through an add-on controller 27. The add-on unit 28 is connected to an external local area network (LAN) 30 or the like in accordance with an arbitrary connection procedure (protocol).

[0022] Part of the setting data written in the memory register 21 and the add-on register 25 is determined from the add-on unit 28 by a system control circuit 29. In accordance with the determination, a function to be executed by the electronic device 200 is changed. Specifically, in accordance with the determination, for example, the system control circuit 29 supplies a control signal for driving the memory controller 23 or the add-on controller 27. At the same time, the function being executed using the memory register 21 or the add-on register 25 is reported.

[0023] ~~Fig. 3 shows an example of the structure of the memory register 21. As shown in Fig. 3, the memory register 21 includes addresses "00" to "IF" (where "I" represents a hexadecimal value). Each address includes a 2-byte read register and a 2-byte write register. Information in the electronic device 200 is read into the read register, whereas information from the main unit 100 is written in the write register.~~

[0024] For example, the address "00" in the memory register 21 is unused. The read register at the address "01" is an interrupt register. The read registers at the addresses "02" and "03" are status 0 and 1 registers, respectively. The

write registers at the addresses "01" to "03" are unused. The read register and the write register at the address "04" are type registers. The address "05" is unused.

[0025] The read register and the write register at the address "06" are category number registers. The read register at the address "07" is a class number register. The write register at the address "07" is unused. The address "08" is unused. The read registers and the write registers at the addresses "09" to "0E" are undefined. The read register at the address "0F" is undefined, and the write register at the address "0F" is unused.

[0026] The write register at the address "10" is a system parameter register. The write registers at the addresses "11" to "13" are registers for block addresses 2 to 0, respectively. The write register at the address "14" is a command parameter register. The read registers at the addresses "10" to "14" are unused. The read register and the write register at the address "15" are page address registers.

[0027] The read register and the write register at the address "16" are registers for block flag data. The read register and the write register at the address "17" are block information registers. The read registers and the write registers at the addresses "18" and "19" are registers for logical addresses 1 and 0, respectively. Each of the addresses from "1A" to "1E" is for formulating a format. The address "1F" is unused.

[0028] In order for the main unit 100 to change a function to be executed by the electronic device 200, the code (category number) of a desired function is written in the write register at the address "06". The system control circuit 29 reads the code (category number) of the function from the write register and, hence, the function requested by the main unit 100 is detected by the electronic device 200.

[0029] In order for the electronic device 200 to execute the detected function, for example, the system control circuit 29 generates a control signal for driving the memory controller 23 or the add-on controller 27. The control signal generated by the system control circuit 29 is supplied to the memory controller 23 or the add-on controller 27 and, as a result, the function requested by the main unit 100 is executed by the electronic device 200.

[0030] The code (category number) of the function to be executed by the electronic device 200 is read in the read register at the address "06". At the same time, a class number for indicating a version, which is associated with the function executed by the electronic device 200, is read in the read register at the address "07". The main unit 100 determines the category number and the class number, and therefore recognizes the function to be executed by the electronic device 200. The add-on driver 12 which corresponds to the recognized function is activated.

[0031] In this way, the code (category number) of the desired function is written by the main unit 100 in the write register at the address "06", and hence the function to be executed by the electronic device 200 is changed. The code (category number) of the function to be executed by the electronic device 200 is read in the read register at the address "06", and hence the function to be executed by the electronic device 200 is recognized by the main unit 100. Thus, the changing of the function is smoothly performed.

[0032] Specifically, when the function written by the main unit 100 in the write register at the address "06" is not installed in the electronic device 200, the code (category number) of the function is not read in the read register at the address "06". Thus, the main unit 100 recognizes that the desired function is not installed in the electronic device 200. Accordingly, the main unit 100 can smoothly perform

processing such as cancellation of the execution of the function.

[0033] For example, common addresses are set in the memory register 21 and the add-on register 25. At the same time, registers in accordance with the respective functions are enabled by bank switching or the like. In this case, the contents of the registers are changed in accordance with each function. However, the type register at the address "04", the category number register at the address "06", and the class number in the read register at the address "07" are common to all functions.

[0034] The addresses "00" to "FF" can be provided for these registers as a whole. For example, the memory register 21 is provided at the addresses "00" to "1F", and the add-on register 25 is provided at the addresses "20" to "3F". By controlling high-order bits of these addresses, the changing of the register can be performed. In this case, high-order bits of the addresses "04", "06", and "07" are ignored, and hence these addresses can be shared among all functions.

[0035] According to this embodiment, a predetermined section of a register for performing setting is provided with a write area in which the code of a function requested by a main unit is written, and a read area in which the code of a function selected in an electronic device and a code associated therewith are read. In this way, the main unit can recognize a function executed by the electronic device by using a simple method. Hence, a desired function can be reliably executed, and the main unit can reliably enable a desired function.

[0036] In a conventional electronic device, erroneous processing and main unit malfunctions result from the inability of the main unit to recognize the function executed by the electronic device. When a function is not executed, the main unit may go down, and it may require a complicated

procedure and time to restore the main unit to it's original state. According to the present invention, the above problems are easily solved.

[0037] Fig. 4 shows an example of a manner in which the code (category number) of a function used at the address "06" and the class number used at the address "07" are defined in this embodiment. Specifically, referring to Fig. 4, the category number "00" represents the function of a memory. In this case, the class number only has the value "00". Since the addresses "06" and "07" in a conventional memory are unused, the value "00" is assigned to each of these addresses.

[0038] In contrast, a new category number "01" represents the function of an information device. A category number "02" represents the function of a LAN (Ethernet) connecting device or the like. A category number "03" represents the function of a communication device. A category number "04" represents the function of a communication device (Bluetooth) which is defined by manufactures in Japan, the United States, and Europe as a common standard. To each of these functions, a class number from the value "01" onward is assigned.

[0039] In addition to the above-described functions, the electronic device 200 may include other functions such as a camera function, a display function, a global positioning system (GPS) function, and a serial communication function.

[0040] For example, functions installed in the electronic device 200 can be listed in the numerical order of their category numbers and class numbers, and the list can be provided in the electronic device 200. It is therefore possible to enable the main unit 100 to recognize in advance the function to be executed by the electronic device 200 and to prevent unnecessary operations and malfunctions of the main unit 100. To this end, when the electronic device 200 has a memory function, the above list of functions may be installed in the memory of the electronic device 200.

[0041] Specifically, when the electronic device 200 has a memory function, a boot block format of the memory is shown in Fig. 5.

[0042] Referring to Fig. 5, page 0 consists of a 368-byte header, a 48-byte system entry, and a 96-byte boot & attribute information. Page 1 includes disabled block data. Page 2 includes a 256-byte card information structure (CIS) and a 256-byte identity drive information (IDI). Page 3 includes a function list information entry in which a list of installed functions is recorded.

[0043] The header on page 0 includes information such as the block ID, a format version, and the number of entries. As shown in Fig. 6, the system entry includes information concerning data from page 1 onward. Specifically, a start position for the disabled block data is provided in the first 4 bytes of the system entry. In the next 4 bytes, the data size of the disabled block data is provided. In the next 1 byte, the ID of the disabled block data is provided. The next 3 bytes are undefined.

[0044] Definitions of the CIS/IDI data are provided in the next 12 bytes of the system entry, in the same manner as the information concerning the disabled block data. Specifically, a start position for the CIS/IDI data is provided in the first 4 bytes of the 12 bytes. In the next 4 bytes, the data size of the CIS/IDI data is provided. In the next 1 byte, the ID of the CIS/IDI data is provided. The next 3 bytes are undefined.

[0045] In the subsequent 12 bytes, data concerning the function list information entry is provided. Specifically, a start position for the function list data is provided in first 4 bytes of the 12 bytes. In the next 4 bytes, the data size of the function list data is provided. In the next 1 byte, the ID of the function list data is provided. The next 3 bytes are undefined. In addition, the next 12 bytes are

undefined. In this manner, definitions of the function list data are provided in the system entry area.

[0046] At an address (page 3) of the memory in accordance with the function list data definitions, function list data such as shown in Fig. 7 is provided. Specifically, in the function list data, a category number indicating an installed function and a class number corresponding to the category number are provided. Fig. 7 shows a case in which the function of a LAN (Ethernet) connecting device and the function of a communication device (Bluetooth) are provided.

[0047] Thus, in the category number section, "02" indicating the function of the LAN (Ethernet) connecting device and "04" indicating the function of the communication device (Bluetooth) are provided. In the corresponding class number sections, "01" is provided. The category number and the class number each consist of 1 byte. The data size of the function list data in the system entry area is obtained by 2 bytes (number of installed functions).

[0048] In order for the main unit 100 to obtain the function list data in the electronic device 200, the main unit 100 assumes that the electronic device 200 has a memory function and reads the system entry area. If reading is accomplished, it is possible to determine that the electronic device 200 at least has a memory function. In accordance with the definitions provided in the system entry area, the function list data is read. As a result, the function list data in the electronic device 200 can be obtained.

[0049] In contrast, when reading of the system entry area is not accomplished, it is determined that the electronic device 200 does not have a memory function. In this state, it is impossible to obtain the function list data. In such a case, the main unit 100 writes the category numbers one by one in the write register at the address "06". When the same category number is returned to the read register at the

address "06", it is possible to determine that the function is installed in the electronic device 200.

[0050] Accordingly, the main unit 100 can recognize in advance functions installed in the electronic device 200. It is thus possible to prevent unnecessary operations and malfunctions of the main unit 100. In other words, the main unit 100 checks a recognized function installed in the electronic device 200 with a function of the main unit and determines that only matching functions are executable. Hence, it is possible to prevent unnecessary operations and malfunctions such as the activation of an unexecutable function.

[0051] The functions installed in the electronic device 200 can be recognized by having the main unit 100 write the category numbers included in the main unit 100 one by one in the write register at the address "06". A determination is then made as to whether the same category number is returned to the read register at the address "06". If the determination is affirmative, it is determined that the function is installed in the electronic device 200. Before performing necessary functions, recognition of the functions can be performed in a similar manner.

[0052] According to this embodiment, an electronic device is removably provided with respect to a main unit. The electronic device exchanges arbitrary data with the main unit and performs a plurality of functions. The electronic device has a register for performing setting when a function is executed. A predetermined section of the register is provided with a write area in which the code of a function requested by the main unit is written, and a read area in which the code of a function selected in the electronic device and the code associated therewith are read. As a result, it is possible to enable the main unit to easily recognize a function to be

executed by the electronic device, and hence the main unit can reliably perform a desired function.

[0053] Although the invention herein has been described with reference to particular embodiments, it is to be understood that these embodiments are merely illustrative of the principles and applications of the present invention. It is therefore to be understood that numerous modifications may be made to the illustrative embodiments and that other arrangements may be devised without departing from the spirit and scope of the present invention as defined by the appended claims.